	Application No.	Applicant(s)
Examiner-Initiated Interview Summary	10/662,122	SARMAVUORI, JUHA
	Examiner	Art Unit
	LEILA MALEK	2611

All Participants:

Status of Application: _____

(1) *LEILA MALEK*.

(3) *Brad Chin* .

(2) *Majid AlBassam* .

(4) _____.

Date of Interview: *13 July 2010*

Time: _____

Type of Interview:
☒ Telephonic
☐ Video Conference
☐ Personal (Copy given to: ☐ Applicant   ☐ Applicant's representative)

Exhibit Shown or Demonstrated: ☐ Yes   ☐ No
   If Yes, provide a brief description: .

Part I.

Rejection(s) discussed:
N/A

Claims discussed:
1,8,9,10,11,12,20,21,22,23,32,33,34

Prior art documents discussed:

Part II.

SUBSTANCE OF INTERVIEW DESCRIBING THE GENERAL NATURE OF WHAT WAS DISCUSSED:
See Continuation Sheet

Part III.

☒ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview directly resulted in the allowance of the application. The examiner will provide a written summary of the substance of the interview in the Notice of Allowability.

☐ It is not necessary for applicant to provide a separate record of the substance of the interview, since the interview did not result in resolution of all issues. A brief summary by the examiner appears in Part II above.

(Applicant/Applicant's Representative Signature – if appropriate)

Continuation of Substance of Interview including description of the general nature of what was discussed: The application has been amended as follows:

a. The last paragraph of claim 1 (see the paragraph that starts with "in the event that"), has been replaced as follows:
in the event that an octet slip before the first error position is not indicated, searching in the searching block for a second error bit to identify a second error position and detecting, with a detector, octet slip by verifying bits starting from a position in an adjacent block corresponding to the second error bit position;
detecting if the bits of the adjacent block starting from the second error bit position are verified as being correct; and detecting that the octet slip is between the first and second error bit positions if the bits starting from the second error bit position are correct.

b. In claim 1, "and" has been deleted from the end of the paragraph started with "counting, with a counter"

c. In claims 8, one of the periods at the end of the sentence has been removed

d. Claims 9 and 10 have been cancelled.

e. Claim 11, now depends on claim 1.

f. The last paragraph of claim 12 (see the paragraph that starts with "detect the octet slip by verifying") has been replaced as follows:
detect the octet slip by verifying error bits starting from a position in an adjacent block corresponding to a second error bit position of a second error bit in the signaling block in the event that an octet slip before the first error position is not indicated,
detect if the bits of the adjacent block starting from the second error bit position are correct, and
detect that the octet slip is between the first and second error bit positions if the bits starting from the second error bit are correct.

g. Claims 20 and 21 have been cancelled.

h. Claim 22 now depends on claim 12.

i. The last paragraph (see the paragraph that starts with "detect octet slip of the signal transmitted") of claim 23, has been replaced as follows:
detect octet slip of the signal transmitted from the sender terminal through the in path equipment to the receiver terminal, in the event that octet slip has occurred in the signal such that the configuration of the slip detector is capable of detecting it,
detect if the bits of the adjacent block starting from the second error bit position are verified as being correct, and
detect that the octet slip is between the first and second error bit positions if the bits starting from the second error bit position are correct.

j. Claims 32 and 33 have been cancelled.

k. Claim 34, now depends on claim 23..